

Feedback Report a problem Satisfaction survey

Terms used reconfigurable fpga data collection instrument

Found 49,558 of 147,060

Sort
results by
Display
results
Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

Display
results

¹ An SBus monitor board

H. A. Xie, K. E. Forward, K. M. Adams, D. Leask

February 1995 Proceedings of the 1995 ACM third international symposium on Field-programmable gate arrays

Full text available: pdf(68.64 Additional Information: full citation, abstract, references, index terms

During the development of computer peripherals which interface to the processor via the system bus it is often necessary to acquire the signals on the bus at the hardware level. It is difficult to attach general-purpose logic analysers and incircuit emulators to a multiple pin bus connector and hence it is not practical to catch all the bus data required to ensure that such signals are in accordance with the bus specification. Hence a given connector specific bus monitor board is a necessa ...

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE



Publications/Services Standards Conferences

Welcome **United States Patent and Trademark Office**



Help FAQ Terms IEE	E Peer Review Quick Links Se
Welcome to IEEE Xplore® - Home - What Can I Access? - Log-out	Your search matched 1 of 1099723 documents. A maximum of 500 results are displayed, 50 to a page, sorted by Relevance Descending order. Refine This Search:
Tables of Contents - Journals & Magazines - Conference Proceedings - Standards	You may refine your search by editing the current search expression or enterinew one in the text box. xhwif
Search - By Author - Basic - Advanced - CrossRef Member Services	1 GeneticFPGA: evolving stable circuits on mainstream FPGA devices Levi, D.; Guccione, S.A.; Evolvable Hardware, 1999. Proceedings of the First NASA/DoD Workshop on , 21 July 1999 Pages:12 - 17
O- Join IEEE O- Establish IEEE Web Account O- Access the IEEE Member Digital Library	[Abstract] [PDF Full-Text (56 KB)] IEEE CNF
O- Access the IEEE Enterprise File Cabinet	

Print Format

Home | Log-out | Journals | Conference Proceedings | Standards | Search by Author | Basic Search | Advanced Search | Join IEEE | Web Account |
New this week | OPAC Linking Information | Your Feedback | Technical Support | Email Alerting | No Robots Please | Release Notes | IEEE Online
Publications | Help | FAQ | Terms | Back to Top

Copyright © 2004 IEEE — All rights reserved

IEEE HOME I SEARCH IEEE I SHOP I WEB ACCOUNT I CONTACT IEEE



Membership Publications/Services Standards

Welcome



	2	1 2	Xplore RELEASE 1.8	(8)	Unit
lp	FAQ	Terms	IEEE Peer Review	Qui	k Links

United States Patent and Trademark Office

Walaamaa			Variance
Welcome t	ш	-	λmm^{*}
L			

C Home

C - What Can I Access?

O- Log-out

Tables of Contents

Journals & Magazines

C Conference Proceedings

Standards

Search

O- By Author

O- Basic

Advanced

CrossRef

Member Services

O- Join IEEE

()- Establish IEEE Web Account

O- Access the **IEEE Member Digital Library**

IEEE Enterprise

O- Access the **IEEE Enterprise File Cabinet**

Your search matched 2 of 1099723 documents.

A maximum of 500 results are displayed, 50 to a page, sorted by Relevance **Descending** order.

Refine This Search:

You may refine your search by editing the current search expression or enterin new one in the text box.

jbits<and>api

☐ Check to search within this result set

Results Key:

JNL = Journal or Magazine CNF = Conference STD = Standard

1 A run-time reconfigurable plug-in for the Winamp MP3 player

Scalera, J.; Jones, M.;

Field-Programmable Custom Computing Machines, 2000 IEEE Symposium on , 19 April 2000

Pages:319 - 320

[Abstract]

[PDF Full-Text (180 KB)]

IEEE CNF

2 High performance DES encryption in Virtex[™] FPGAs using JBits[™]

Patterson, C.;

Field-Programmable Custom Computing Machines, 2000 IEEE Symposium on , 19 April 2000

Pages:113 - 121

[Abstract]

[PDF Full-Text (712 KB)] **IEEE CNF**

Print Format

Home | Log-out | Journals | Conference Proceedings | Standards | Search by Author | Basic Search | Advanced Search | Join IEEE | Web Account | New this week | OPAC Linking Information | Your Feedback | Technical Support | Email Alerting | No Robots Please | Release Notes | IEEE Online Publications | Help | FAQ| Terms | Back to Top

Copyright © 2004 IEEE — All rights reserved

Ref #	Hits	Search Query	DBs	Default Operator	Piurals	Time Stamp
L1	1	"5603043".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/12/07 08:32
L2	40	("5603043").URPN.	USPAT	OR	OFF	2004/12/07 08:32
L3	34	("5603043").URPN. not ((national and instrument).as.)	USPAT	OR	ON	2004/12/07 08:52
L4	1711	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same ((data adj collector) or measurement or metric or meta-data or (meta adj data)) and (debug\$4 or test\$3 or verify or verification or validify or validification or co-verification)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 08:54
L5	539	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same ((data_adj_collector) or measurement or metric or meta-data or (meta adj_data)) same (debug\$4 or test\$3 or verify or verification or validify or validification or co-verification)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 08:55
L6	154	717/124 and (programmable or reconfigurable or fpga)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/07 08:56
i-7		(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same ((data adj collector) or measurement or metric or meta-data or (meta adj data)) same (debug\$4 or test\$3 or verify or verification or validify or validification or co-verification) and 717/??? not ((national adj instrument).as.)	US PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ŌR	ÓN	2004/12/07 09:33
L8	40	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same ((data adj collector) or measurement or metric or meta-data or (meta adj data)) same (debug\$4 or test\$3 or verify or verification or validify or validification or co-verification) and 714/??? not ((national adj instrument).as.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 09:34

L9	1	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same ((data adj collector) or measurement or metric or meta-data or (meta adj data)) same (debug\$4 or test\$3 or verify or verification or validify or validification or co-verification) and 716/??? not ((national adj instrument).as.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 09:41
L10	671	716/3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON The state of th	2004/12/07 09:41
L11	2231	716/4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 09:42
L13	730	716/16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 09:42
L14	826	716/17	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 09:42
L15	1255	716/18	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 09:42
L16	0	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same sensor same (test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) and 716/3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 09:44
L17	56	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same (test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) and 716/3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 09:44
L18	183	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same (test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) and 716/4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 09:44

f	T				Γ	
L19	208	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same (test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) and 716/16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 09:44
L20	138	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same (test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) and 716/17	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 09:44
L21	113	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same (test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) and 716/18	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 09:48
L22	56	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same (test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) and 716/3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 09:48
L23	110	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same (test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) and 714/25	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 09:48
L24	57	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same (test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) and 714/33	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 09:48
L25	58	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same (test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) and 714/39	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 09:49
. S1	633	717/124	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/06 07:35
S2	633	717/124	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/02 16:12
S4	132	717/124 and (programmable or reconfigurable or re-configurable or fpga) and (test\$3 or debug\$4 or simulat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR_	OFF	2004/12/02 16:16

S5	88	717/124 and (programmable or reconfigurable or re-configurable or fpga) and (test\$3 or debug\$4 or simulat\$3) and plurality	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/02 16:16
S6	3	717/124 and (programmable or reconfigurable or re-configurable or fpga) and (test\$3 or debug\$4 or simulat\$3) and (plurality near5 hardware or hard-ware)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/02 16:23
S7	2	debug\$4 near3 (embedded adj program) and (programmable or reconfiguarble or re-configurable or fpga or eeprom or eprom)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/02 16:26
S8	5,	debug\$4 near3 (hardware adj configuration) and (programmable or reconfiguarble or re-configurable or fpga or eeprom or eprom)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/02 16:29
S9	6	("6226776" "6009256" "6219628"). pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/02 16:29
S10	1	("5197016" "5437037" "5493507" "5530843" "5603043" "5748488" "5953519").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	-2004/12/02 16:31
S11	33	("5493507").URPN.	USPAT	OR	OFF	2004/12/02 16:40
S12	1	("6298453").URPN.	USPAT	OR	OFF	2004/12/02 16:52
S13	19	("6052524").URPN.	USPAT	OR	OFF	2004/12/02 17:15
S14	6934	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same sensor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 07:37
S15	833	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same sensor same (test\$3 or debug\$4 or verify or verification or analysis or analyz\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 07:50
S16	3	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same sensor same (test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) and 717/???	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 07:45

S17	0	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same sensor same (test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) and 716/???	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 07:39
S18	2	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same sensor same (test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) and 712/???	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 07:50
S19	15	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same sensor same (test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) and 714/???	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 07:40
S20	0	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same sensor same (test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) and 703/???	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ΘN	2004/12/06 07:40
S21	30	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same sensor same (test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) and (hardware adj configuration)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 07:42
S22	969	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same sensor and ((test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) near3 (sensor or peripheral or hardware or attached or remote))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 07:49
S23	9	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same sensor and ((test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) near3 (sensor or peripheral or hardware or attached or remote)) and 717/???	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 07:45

		T				
S24	954	(reconfigurable or reprogrammable or re-programmable or eeprom or eprom or fpga) same sensor and ((test\$3 or debug\$4 or verify or verification or analysis or analyz\$5) near3 (sensor or peripheral or hardware or attached or remote)) not ((national and instruments).as.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 07:49
S25	824	(reconfigurable or reprogrammable or re-programmable or eeprom or	US-PGPUB; USPAT;	OR	ON	2004/12/06 07:50
	3	eprom or fpga) same sensor same	EPO; JPO;			
		(test\$3 or debug\$4 or verify or verification or analysis or analyz\$5)	DERWENT; IBM_TDB			
·		not ((national and instruments).as.	4 1		*** **/	
S26	9	("6107821" "6247147" "6311316" "6366117" "6453456" "6456961" "6460148" "6530071" "6598178").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/12/06 10:27
S27	4	test adj (feed-through)	US-PGPUB; USPAT;	OR	OFF	2004/12/07 08:31
			USOCR			